

Day : Tuesday
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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = XIA

First Name = JINGSONG

Application#	Patent#	Status	Date Filed	Title	Inventor Name 37
60601026	Not Issued	020	08/12/2004	DIGITAL RECEIVER SYSTEM	XIA, JINGSONG
60561085	Not Issued	020	04/09/2004	ADVANCED DIGAL RECEIVER	XIA, JINGSONG
60370458	Not Issued	159	04/05/2002	SYSTEM AND METHOD FOR KERDOCK CODING FOR 2VSB OR QPSK MODULATION	XIA, JINGSONG
60370441	Not Issued	159	04/05/2002	SYSTEM AND METHOD FOR KERDOCK CODING IN FOR 4VSB OR QAM MODULATION	XIA, JINGSONG
60370326	Not Issued	159	04/05/2002	SYMBOL CLOCK RECOVERY ALGORITHM FOR DTV RECEIVERS	XIA, JINGSONG
60370296	Not Issued	159	04/05/2002	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT	XIA, JINGSONG
60370295	Not Issued	159	04/05/2002	DATA-DIRECTED FREQUENCY ACQUISITION LOOP	XIA, JINGSONG
60370283	Not Issued	159	04/05/2002	DATA-DIRECTED FREQUENCY-AND-PHASE-LOCK LOOP	XIA, JINGSONG
60369716	Not Issued	159	04/04/2002	CARRIER RECOVERY ALGORITHM FOR DTV RECEIVERS	XIA, JINGSONG
60248180	Not Issued	159	11/13/2000	EQUALIZER FOR TIME DOMAIN SIGNAL PROCESSING	XIA, JINGSONG

<u>10701047</u>	Not Issued	030	11/04/2003	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER	XIA, JINGSONG
<u>10650179</u>	Not Issued	041	08/28/2003	GHOST ELIMINATING EQUALIZER	XIA, JINGSONG
<u>10408053</u>	Not Issued	041	04/04/2003	CARRIER RECOVERY FOR DTV RECEIVERS	XIA, JINGSONG
<u>10407634</u>	Not Issued	041	04/04/2003	SYSTEM AND METHOD FOR SYMBOL CLOCK RECOVERY	XIA, JINGSONG
<u>10404553</u>	Not Issued	030	04/01/2003	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT	XIA, JINGSONG
<u>10404516</u>	Not Issued	092	04/01/2003	DATA-DIRECTED FREQUENCY ACQUISITION LOOP	XIA, JINGSONG
<u>10404511</u>	Not Issued	089	04/01/2003	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP	XIA, JINGSONG
<u>10167194</u>	Not Issued	161	06/11/2002	TRANSMISSION AND RECEPTION SYSTEM WITH GUARD INTERVALS CONTAINING KNOWN COMPONENTS UNRELATED TO TRANSMITTED DATA	XIA, JINGSONG
<u>10011333</u>	6563436	150	12/03/2001	KERDOCK CODING AND DECODING SYSTEM FOR MAP DATA	XIA, JINGSONG
<u>10007999</u>	Not Issued	030	11/13/2001	EQUALIZER FOR TIME DOMAIN SIGNAL PROCESSING	XIA, JINGSONG
<u>09884256</u>	Not Issued	071	06/19/2001	COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER	XIA, JINGSONG
<u>09876547</u>	Not Issued	071	06/07/2001	ERROR GENERATION FOR ADAPTIVE EQUALIZER	XIA, JINGSONG
<u>09875720</u>	6829297	150	06/06/2001	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER	XIA, JINGSONG

<u>09808585</u>	6725410	150	03/14/2001	INTERATIVE DECODER	XIA, JINGSONG
<u>09572799</u>	Not Issued	061	05/17/2000	COMPOUND CHIRP AND SYNCHRONIZER FOR USING SAME	XIA, JINGSONG
<u>09545716</u>	6731682	150	04/07/2000	MULITIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	XIA, JINGSONG
<u>09545597</u>	Not Issued	161	04/07/2000	MULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	XIA, JINGSONG
<u>09545447</u>	Not Issued	071	04/07/2000	MULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	XIA, JINGSONG
<u>09545445</u>	6754262	150	04/07/2000	MULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	XIA, JINGSONG
<u>09425522</u>	6650700	150	10/22/1999	DUAL PATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	XIA , JINGSONG
<u>09283877</u>	6700930	150	04/01/1999	HOST ELIMINATING EQUALIZER	XIA , JINGSONG
<u>09158730</u>	6442221	150	09/22/1998	HOST ELIMINATING EQUALIZER	XIA , JINGSONG
<u>08831236</u>	5949768	150	04/02/1997	TWO TIER ADAPTIVE RANDOM ACCESS PROTOCOL	XIA , JINGSONG
<u>08777217</u>	5978382	150	12/27/1996	ADAPTIVE RANDOM ACCESS PROTOCOL AND FIXED SEARCH TREE EXPANSION RESOLUTION FOR MULTIPLE STATION NETWORKS	XIA , JINGSONG
<u>08777216</u>	6034967	150	12/27/1996	ADAPTIVE RANDOM ACCESS PROTOCOL AND DYNAMIC SEARCH TREE EXPANSION RESOLUTION FOR MULTIPLE STATION NETWORKS	XIA , JINGSONG
<u>08734909</u>	5956325	150	10/22/1996	ADAPTIVE RANDOM ACCESS PROTOCOL FOR CATV NETWORKS	XIA , JINGSONG
<u>08734908</u>	6002691	150	10/22/1996	DYNAMIC SEARCH TREE	XIA , JINGSONG

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Inventor Search Completed: No Records to Display.

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Day : Tuesday
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PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = CITTA

First Name = RICHARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>60370326</u>	Not Issued	159	04/05/2002	SYMBOL CLOCK RECOVERY ALGORITHM FOR DTV RECEIVERS	CITTA, RICHARD W.
<u>60370296</u>	Not Issued	159	04/05/2002	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT	CITTA, RICHARD W.
<u>60370295</u>	Not Issued	159	04/05/2002	DATA-DIRECTED FREQUENCY ACQUISITION LOOP	CITTA, RICHARD W.
<u>60370283</u>	Not Issued	159	04/05/2002	DATA-DIRECTED FREQUENCY-AND-PHASE-LOCK LOOP	CITTA, RICHARD W.
<u>60369716</u>	Not Issued	159	04/04/2002	CARRIER RECOVERY ALGORITHM FOR DTV RECEIVERS	CITTA, RICHARD W.
<u>60255476</u>	Not Issued	159	12/13/2000	ROBUST VSB TRANSMISSION SYSTEM	CITTA, RICHARD
<u>60255464</u>	Not Issued	159	12/13/2000	ROBUST VSB ENHANCED SLICE PREDICTION AND EQUALIZER FEEDBACK	CITTA, RICHARD
<u>60248180</u>	Not Issued	159	11/13/2000	EQUALIZER FOR TIME DOMAIN SIGNAL PROCESSING	CITTA, RICHARD W.
<u>10815335</u>	Not Issued	030	04/01/2004	CODE ENHANCED EQUALIZATION BASED UPON A RELIABILITY FACTOR	CITTA, RICHARD W.
<u>10763488</u>	Not Issued	020	01/23/2004	ROBUST DIGITAL COMMUNICATION SYSTEM	CITTA, RICHARD W.
<u>10756094</u>	Not Issued	168	01/13/2004	ROBUST DIGITAL COMMUNICATION SYSTEM	CITTA, RICHARD W.

<u>10701047</u>	Not Issued	030	11/04/2003	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER	CITTA, RICHARD W.
<u>10650179</u>	Not Issued	041	08/28/2003	GHOST ELIMINATING EQUALIZER	CITTA, RICHARD W.
<u>10408053</u>	Not Issued	041	04/04/2003	CARRIER RECOVERY FOR DTV RECEIVERS	CITTA, RICHARD W.
<u>10407634</u>	Not Issued	041	04/04/2003	SYSTEM AND METHOD FOR SYMBOL CLOCK RECOVERY	CITTA, RICHARD W.
<u>10404553</u>	Not Issued	030	04/01/2003	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT	CITTA, RICHARD W.
<u>10404516</u>	Not Issued	092	04/01/2003	DATA-DIRECTED FREQUENCY ACQUISITION LOOP	CITTA, RICHARD W.
<u>10404511</u>	Not Issued	089	04/01/2003	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP	CITTA, RICHARD W.
<u>10340973</u>	Not Issued	071	01/13/2003	CODE ENHANCED EQUALIZATION BASED UPON A RELIABILITY FACTOR	CITTA, RICHARD W.
<u>10167194</u>	Not Issued	161	06/11/2002	TRANSMISSION AND RECEPTION SYSTEM WITH GUARD INTERVALS CONTAINING KNOWN COMPONENTS UNRELATED TO TRANSMITTED DATA	CITTA, RICHARD W.
<u>10011333</u>	6563436	150	12/03/2001	KERDOCK CODING AND DECODING SYSTEM FOR MAP DATA	CITTA, RICHARD W.
<u>10007999</u>	Not Issued	030	11/13/2001	EQUALIZER FOR TIME DOMAIN SIGNAL PROCESSING	CITTA, RICHARD W.
<u>09982723</u>	6647074	150	10/18/2001	REMOVAL OF CLOCK RELATED ARTIFACTS FROM AN OFFSET QAM GENERATED VSB SIGNAL	CITTA, RICHARD W.
<u>09884256</u>	Not Issued	071	06/19/2001	COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER	CITTA, RICHARD W.
<u>09876547</u>	Not	071	06/07/2001	ERROR GENERATION FOR	CITTA, RICHARD

	Issued			ADAPTIVE EQUALIZER	W.
<u>09875720</u>	6829297	150	06/06/2001	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER	CITTA, RICHARD W.
<u>09808585</u>	6725410	150	03/14/2001	INTERATIVE DECODER	CITTA, RICHARD W.
<u>09804261</u>	Not Issued	030	03/13/2001	ROBUST DIGITAL COMMUNICATION SYSTEM	CITTA, RICHARD W.
<u>09804237</u>	Not Issued	041	03/13/2001	ENHANCED SLICE PREDICTION FEEDBACK	CITTA, RICHARD W.
<u>09572799</u>	Not Issued	061	05/17/2000	COMPOUND CHIRP AND SYNCHRONIZER FOR USING SAME	CITTA, RICHARD W.
<u>09572122</u>	Not Issued	041	05/17/2000	CODE ENHANCED EQUALIZATION BASED UPON A RELIABILITY FACTOR	CITTA, RICHARD W.
<u>09545716</u>	6731682	150	04/07/2000	ULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	CITTA, RICHARD W
<u>09545597</u>	Not Issued	161	04/07/2000	MULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	CITTA, RICHARD W.
<u>09545447</u>	Not Issued	071	04/07/2000	MULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	CITTA, RICHARD W.
<u>09545445</u>	6754262	150	04/07/2000	MULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	CITTA, RICHARD W.
<u>07453525</u>	5016100	150	12/20/1989	TRANSMISSION OF A VIDEO SIGNAL USING ADAPTIVE DELTA MODULATION	CITTA , RICHARD W.
<u>07442474</u>	4947429	250	11/22/1989	PAY PER VIEW TELEVISION SIGNALING METHOD	CITTA , RICHARD W.
<u>07408158</u>	5040063	150	10/23/1989	TV SIGNAL TRANSMISSION SYSTEMS AND METHODS	CITTA , RICHARD W.
<u>07408152</u>	5144431	150	09/15/1989	TELEVISION SIGNAL TRANSMISSION SYSTEM WITH TEMPORAL PROCESSING	CITTA , RICHARD W.

<u>07407596</u>	<u>5136381</u>	150	09/15/1989	TV SIGNAL TRANSMISSION SYSTEMS AND METHODS	CITTA , RICHARD W.
<u>07370222</u>	<u>5043805</u>	150	06/22/1989	TV SIGNAL TRANSMISSION SYSTEMS AND METHODS	CITTA , RICHARD W.
<u>07343103</u>	<u>4944006</u>	250	04/25/1989	SECURE DATA PACKET TRANSMISSION SYSTEM AND METHOD	CITTA , RICHARD W.
<u>07238956</u>	Not Issued	168	08/31/1988	TV SIGNAL TRANSMISSION SYSTEMS AND METHODS	CITTA , RICHARD W.
<u>07228905</u>	Not Issued	166	08/04/1988	TELEVISION SIGNAL SCRAMBLING SYSTEM AND METHOD	CITTA , RICHARD W.
<u>07221166</u>	<u>4876718</u>	150	07/19/1988	SECURE DATA PACKET TRANSMISSION SYSTEM AND METHOD	CITTA , RICHARD W.
<u>07159813</u>	<u>4885776</u>	250	02/24/1988	TELEVISION SIGNAL ENCODER WITH REDUCED TRANSIENT SWITCHING EFFECTS	CITTA , RICHARD W.
<u>07126452</u>	Not Issued	166	11/30/1987	PAY PER VIEW TELEVISION SIGNALLING METHOD	CITTA , RICHARD W.
<u>07025240</u>	<u>4755762</u>	150	03/12/1987	COMBINED FPLL AND PSK DATA DETECTOR	CITTA , RICHARD W.
<u>07025236</u>	<u>4771458</u>	150	03/12/1987	SECURE DATA PACKET TRANSMISSION SYSTEM AND METHOD	CITTA , RICHARD W.
<u>07019445</u>	<u>4802213</u>	150	02/26/1987	SOUND MASKING IN PHASE REVERSAL TV SCRAMBLING SYSTEM	CITTA , RICHARD W.
<u>07014893</u>	<u>4759061</u>	150	02/13/1987	TECHNIQUE FOR TOGGLING CODING MODE IN A TELEVISION SIGNAL ENCODER	CITTA , RICHARD W.

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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = LOPRESTO

First Name = SCOTT

Application#	Patent#	Status	Date Filed	Title	Inventor Name 26
<u>60370326</u>	Not Issued	159	04/05/2002	SYMBOL CLOCK RECOVERY ALGORITHM FOR DTV RECEIVERS	LOPRESTO, SCOTT M.
<u>60370296</u>	Not Issued	159	04/05/2002	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT	LOPRESTO, SCOTT M.
<u>60370295</u>	Not Issued	159	04/05/2002	DATA-DIRECTED FREQUENCY ACQUISITION LOOP	LOPRESTO, SCOTT M.
<u>60370283</u>	Not Issued	159	04/05/2002	DATA-DIRECTED FREQUENCY-AND-PHASE-LOCK LOOP	LOPRESTO, SCOTT M.
<u>60369716</u>	Not Issued	159	04/04/2002	CARRIER RECOVERY ALGORITHM FOR DTV RECEIVERS	LOPRESTO, SCOTT M.
<u>60248180</u>	Not Issued	159	11/13/2000	EQUALIZER FOR TIME DOMAIN SIGNAL PROCESSING	LOPRESTO, SCOTT M.
<u>10701047</u>	Not Issued	030	11/04/2003	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER	LOPRESTO, SCOTT M.
<u>10650179</u>	Not Issued	041	08/28/2003	GHOST ELIMINATING EQUALIZER	LOPRESTO, SCOTT M.
<u>10408053</u>	Not Issued	041	04/04/2003	CARRIER RECOVERY FOR DTV RECEIVERS	LOPRESTO, SCOTT M.
<u>10407634</u>	Not Issued	041	04/04/2003	SYSTEM AND METHOD FOR SYMBOL CLOCK RECOVERY	LOPRESTO, SCOTT M.

<u>10404553</u>	Not Issued	030	04/01/2003	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT	LOPRESTO, SCOTT M.
<u>10404516</u>	Not Issued	092	04/01/2003	DATA-DIRECTED FREQUENCY ACQUISITION LOOP	LOPRESTO, SCOTT M.
<u>10404511</u>	Not Issued	089	04/01/2003	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP	LOPRESTO, SCOTT M.
<u>10007999</u>	Not Issued	030	11/13/2001	EQUALIZER FOR TIME DOMAIN SIGNAL PROCESSING	LOPRESTO, SCOTT M.
<u>09884256</u>	Not Issued	071	06/19/2001	COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER	LOPRESTO, SCOTT M.
<u>09876547</u>	Not Issued	071	06/07/2001	ERROR GENERATION FOR ADAPTIVE EQUALIZER	LOPRESTO, SCOTT M.
<u>09875720</u>	6829297	150	06/06/2001	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER	LOPRESTO, SCOTT M.
<u>09627916</u>	6504872	150	07/28/2000	DOWN-CONVERSION DECODER FOR INTERLACED VIDEO	LOPRESTO, SCOTT M.
<u>09545716</u>	6731682	150	04/07/2000	MULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	LOPRESTO, SCOTT M
<u>09545597</u>	Not Issued	161	04/07/2000	MULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	LOPRESTO, SCOTT M.
<u>09545447</u>	Not Issued	071	04/07/2000	MULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	LOPRESTO, SCOTT M.
<u>09545445</u>	6754262	150	04/07/2000	MULTIPATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	LOPRESTO, SCOTT M.
<u>09464279</u>	6510178	150	12/15/1999	COMPENSATING FOR DRIFT IN THE DOWN CONVERSION	LOPRESTO , SCOTT M.

				OF HIGH DEFINITION SEQUENCES TO LOWER RESOLUTION SEQUENCES	
<u>09460645</u>	<u>6628714</u>	150	12/15/1999	DOWN CONVERTING MPEG ENCODED HIGH DEFINITION SEQUENCES TO LOWER RESOLUTION WITH REDUCED MEMORY IN DECODER LOOP	LOPRESTO , SCOTT M.
<u>09425522</u>	<u>6650700</u>	150	10/22/1999	DUAL PATH GHOST ELIMINATING EQUALIZER WITH OPTIMUM NOISE ENHANCEMENT	LOPRESTO , SCOTT M.
<u>09283877</u>	<u>6700930</u>	150	04/01/1999	GHOST ELIMINATING EQUALIZER	LOPRESTO , SCOTT M.

Inventor Search Completed: No Records to Display.

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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = ZHANG

First Name = WENJUN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 30
<u>60463934</u>	Not Issued	159	04/18/2003	CYCLIC AMP RESPONSE ELEMENT ACTIVATOR PROTEINS AND USES RELATED THERETO	ZHANG, WENJUN
<u>60457695</u>	Not Issued	159	03/26/2003	CYCLIC AMP RESPONSE ELEMENT ACTIVATOR PROTEINS AND USES RELATED THERETO	ZHANG, WENJUN
<u>60401155</u>	Not Issued	159	08/05/2002	METHODS FOR THE TREATMENT OF PATHOLOGICAL CONDITIONS ASSOCIATED WITH INFLAMMATORY CYTOKINES AND COMPOSITIONS THEREFORE	ZHANG, WENJUN
<u>60370458</u>	Not Issued	159	04/05/2002	SYSTEM AND METHOD FOR KERDOCK CODING FOR 2VSB OR QPSK MODULATION	ZHANG, WENJUN
<u>60370441</u>	Not Issued	159	04/05/2002	SYSTEM AND METHOD FOR KERDOCK CODING IN FOR 4VSB OR QAM MODULATION	ZHANG, WENJUN
<u>60370326</u>	Not Issued	159	04/05/2002	SYMBOL CLOCK RECOVERY ALGORITHM FOR DTV RECEIVERS	ZHANG, WENJUN
<u>60369716</u>	Not Issued	159	04/04/2002	CARRIER RECOVERY ALGORITHM FOR DTV RECEIVERS	ZHANG, WENJUN
<u>60298099</u>	Not Issued	159	06/15/2001	ADJUSTABLE PLATFORM FOR PARALLEL KINEMATIC MECHANISM	ZHANG, WENJUN
<u>60248180</u>	Not Issued	159	11/13/2000	EQUALIZER FOR TIME DOMAIN SIGNAL PROCESSING	ZHANG, WENJUN

<u>10739401</u>	Not Issued	030	12/19/2003	FABRICATION OF B/C/N/O/SI DOPED SPUTTERING TARGETS	ZHANG, WENJUN
<u>10701047</u>	Not Issued	030	11/04/2003	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER	ZHANG, WENJUN
<u>10633065</u>	Not Issued	031	08/01/2003	METHODS FOR THE TREATMENT OF PATHOLOGICAL CONDITIONS ASSOCIATED WITH INFLAMMATORY CYTOKINES AND COMPOSITIONS THEREFORE	ZHANG, WENJUN
<u>10449687</u>	Not Issued	030	06/02/2003	SPUTTERING TARGET MATERIALS OF Si-AL FABRICATED USING ATOMIZED POWDER PROCESSING	ZHANG, WENJUN
<u>10449686</u>	Not Issued	030	06/02/2003	FABRICATION OF DUCTILE INTERMETALLIC SPUTTERING TARGETS	ZHANG, WENJUN
<u>10408053</u>	Not Issued	041	04/04/2003	CARRIER RECOVERY FOR DTV RECEIVERS	ZHANG, WENJUN
<u>10407634</u>	Not Issued	041	04/04/2003	SYSTEM AND METHOD FOR SYMBOL CLOCK RECOVERY	ZHANG, WENJUN
<u>10404553</u>	Not Issued	030	04/01/2003	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT	ZHANG, WENJUN
<u>10404516</u>	Not Issued	092	04/01/2003	DATA-DIRECTED FREQUENCY ACQUISITION LOOP	ZHANG, WENJUN
<u>10404511</u>	Not Issued	089	04/01/2003	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP	ZHANG, WENJUN
<u>10282779</u>	Not Issued	071	10/29/2002	FABRICATION OF SINGLE CRYSTAL DIAMOND TIPS AND THEIR ARRAYS	ZHANG, WENJUN
<u>10249522</u>	6795756	150	04/16/2003	METHOD AND SYSTEM FOR CONTROLLING POWER DISTRIBUTION IN A HYBRID FUEL CELL VEHICLE	ZHANG, WENJUN

<u>10243948</u>	Not Issued	071	09/16/2002	REFURBISHING SPENT SPUTTERING TARGETS	ZHANG, WENJUN
<u>10200590</u>	6759005	150	07/23/2002	FABRICATION OF B/C/N/O/SI DOPED SPUTTERING TARGETS	ZHANG, WENJUN
<u>10163620</u>	Not Issued	071	06/07/2002	HIGH-PTF SPUTTERING TARGETS AND METHOD OF MANUFACTURING	ZHANG, WENJUN
<u>10007999</u>	Not Issued	030	11/13/2001	EQUALIZER FOR TIME DOMAIN SIGNAL PROCESSING	ZHANG, WENJUN
<u>09884256</u>	Not Issued	071	06/19/2001	COMBINED TRELLIS DECODER AND DECISION FEEDBACK EQUALIZER	ZHANG, WENJUN
<u>09876547</u>	Not Issued	071	06/07/2001	ERROR GENERATION FOR ADAPTIVE EQUALIZER	ZHANG, WENJUN
<u>09875720</u>	6829297	150	06/06/2001	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER	ZHANG, WENJUN
<u>08121653</u>	Not Issued	161	09/14/1993	ADAPTIVE DPCM CODER	ZHANG , WENJUN
<u>08097523</u>	Not Issued	161	07/27/1993	METHOD OF DETERMINING MOTION VECTORS	ZHANG , WENJUN

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 2
10701047	Not Issued	030	11/04/2003	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER	SONG, HANGBIN
09875720	6829297	150	06/06/2001	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER	SONG, HANGBIN

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